

Accelerator Control Systems

Kazuro Furukawa, KEK

for KEKB and Linac Control Groups

<kazuro.furukawa@kek.jp> Dec. 16. 2008.

1



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Considerations on Accelerator Controls in General

Available Technologies

- Adaptive Reliabilities
- Summary







Definition and goal

Specified only after technical details of the accelerator is decided

¤ Of course the final goal is the science achievement

Often changes after commissioning

- X Many prefer to flexibility as well as to robustness
- **Should support rapid development to realize novel ideas immediately**

≍ Single user system (like BESSIII operation) and Multi-user system (like SOR operation) require very different goals

Unfortunately we don't have general accelerator controls

We may have to create something



History

Discussions of accelerator controls

- At ICALEPCS conferences
 - After some success of NODAL at SPS/CERN
 - X Needs for more general software tools
- NODAL was chosen at TRISTAN
- SLC/SLAC and Fermilab used Micros + VMS (but differently)
- Standard model
 - **Field-network + VME + Unix + X11**
- Software sharing
 - Definition of a Class to represent whole accelerator
 Which was practically impossible
- More common control system with extended API
 - ncRPC/CERN, TACL/CEBAF, ACNET/Tevatron, etc
 - **EPICS** got popular maybe because of the selections at SSC, APS, CEBAF, BESSY, ...
- Then more object oriented software (naturally after RPC)
 - **More computer aided development possible**
 - **CICERO/CERN, TANGO, CORBA+Java, CERN, ...**
 - Windows/Microsoft (ex. Labview) or Linux (ex. ATCA) in Industry, ...



No common controls yet

Balance between many available technologies

- Object-oriented vs. Channel-oriented
 - Object-oriented technology
 - **X** More support benefits from software engineering
 - **Extendable, clearer definitions**
 - **X** Different people have different ideas on control objects

Channel-oriented technology

- **Flat (one-layer structure), simple, scalable**
- **X** Not much support from software engineering
- **¤ Easy to make gateways**





More balances

Compiled language vs. interpretive language

Two level languages

- Interpretive language for rapid prototyping
- Compiled language for established algorithms
- Too much success of NODAL, and SADscript

Compiled languages programmed by expert

- **Documentation, maintenance, policy-driven**
- X Manageable, then reliable

Interpretive/scripting languages

- **Rapid development**
 - Realization of innovative physics ideas in hours
- **Everyone can attend the construction of operation environment**
- Another level of management/maintenance required
 - Because of no policy-enforcing by itself





Best & aggressive vs. moderate & conservative

New technology is attractive

- ¤ But can be a "fad"
- **Can we justify the choice?**

For longer life-span, which is better?

- **Life of accelerator is often very long compared with**
 - •User facilities
 - Commercially available software/communication technologies
- **Comparisonal performance continuously advances**

Accumulation of operation knowledge base

Stored mainly as software and database in the control system
 Beam stabilization algorithms, hardware startup procedures, etc

It is valuable treasure

There should be mechanism to keep such resources

•With longer life-span

8





More balances

- International vs. de-facto standards
 - International organizations pursue ideal solutions
 - **Sometimes they don't become de-facto standards**
 - **Selection of one of many standards is difficult**
 - Watching the market
 - TCP/IP network, Unix/Windows operating system, VME boxes
 - Advantages of de-facto standards
 - **¤ Economical advantage to select products out of markets**
 - **Save man-power avoiding proprietary development**
 - **Solutions will be provided for the old standard in the next generation**
 - ¤As a whole, it is good for long life-span



Available Technologies



PLC

Programmable Logic Controllers (PLC)

Rule-based algorithms (ladder software) can be well-adopted for simple controls

IP network for the both controls and management were preferable

- Especially at KEK/Linac which has a policy of IP-only field network
- Isolated/separated development becomes easy
 - **Outsourcing oriented**
- Equipment developer oriented
 - **Many maintenance capabilities were implemented**
- IEC61131-3 Standards
 - 1 5 languages, with emphasis on naming
 - Effort to make common development environment (XML representation etc.)
 - × Not so popular in Japan, we should pay more attention
- Redundancy

For EPICS-embedded PLC, see the presentation of recent topics



Network with only IP/Ethernet

The policy chosen when we upgrade Linac in 1993

- Make network management simpler
 - **Faster switches, routing, network-booting, etc.**
- Avoid Hardware failure and analysis effort with old field network
 - Home-grown field networks need much dedicated man-power
- Cost for optical Ethernet went down at around 1995
 - **X** Linac has high-power modulator stations, noise source
- Nowadays many facilities have this policy with GbE
 - J-PARC controls basically followed this
- More and more intelligent network devices
 - ⊭ex. Oscilloscopes with Windows/3GHz-Pentium built-in
 - Even EPICS IOC, MATLAB, or others can be embedded
- Network components can be replaced one-by-one
- Security consideration will be more and more important





Another "everywhere" after IP network

Digital circuit and software can be embedded in to one chip

- **Even CPU core is embedded**
- **¤** Flexible and robust, wonderful platform for local controls
 - Sometimes it can be terrible source of bugs
- Nano-second level timing
- More and more gates, memory, pins, etc
- More software support

Embedded-EPICS is possible, see the presentation of recent topics

Available Technologies





Advanced telecommunications computing architecture

- Accommodate many 100ohm serial interconnects
- GbE or PCI-express, 10GbE, etc
- Typically 14slots in 19" and 12-unit height
- Shelf manager manages healthiness of the system
 - through Intelligent Platform Management Interface (IPMI)
- Many reliability improving facilities, redundancy, hot-swap, etc

MicroTCA

More recently defined in 2006, based on AdvancedMC Mezzanine Card defined in ATCA

Begin to have many facilities from ATCA



EPICS

Now is a kind standard, but ...

Object-oriented design support

- Naming scheme, and/or design of new record
- More software-engineering support favored
 - Several different efforts to provide better environment
 Java IOC (M. Kraimer), Control system studio (M. Clausen), Data access (R. Lange)

Security mechanisms

- User, Host-based protection available
- More security
 - **Dynamic controls of security**
 - Access logging

Dynamic configuration of database

- Dynamic creation / loading of records
- Dynamic removal of records

Maybe some part of the codes can be shared with redundant-IOC project

Many other hopes

Available Technologies





Magnet Controls

It is typical controls and still many things to do

Many magnets and many power supplies

- **No one-to-one correspondence**
- Which hardware interface to use

Procedures

Interlock status, on/off, analog with some precision, etc

Energy, kick - field - current conversions

How to represent those conversion curves, many ways even in KEK

Timing synchronous operation

¤ for tune change, orbit correction, etc.

Consistent standardization

Which software level to implement in



Event-based Controls

MRF Event System

 Single Fiber can Transfer Clock, Delayed-Timings (~10ps precision, 8ns step), Events (256), Data Buffers (2k-bytes)

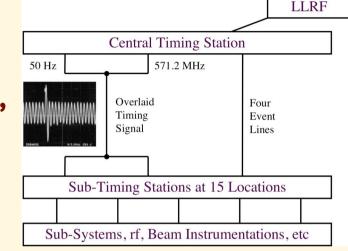
Can replace the old control design concepts with small additions

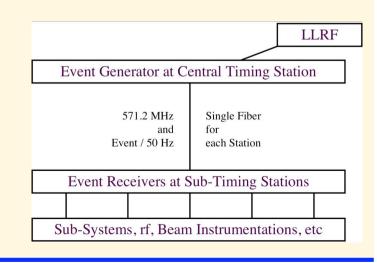
♦ IOC

- RTEMS or VxWorks
- EPICS Driver/Device Support from SLS, LANL etc
- ***VME (MVME5500) or ...**

KEK-Linac migrated from RTEMS to VxWorks to save man-power for RTEMS

May need cost-effective platform







Reliability



Reliability

The end user expect rigid reliable operations Inner layers need flexibilities **¤ Because of daily improvement** hardware Compromise between hardware Interface equipment controls **X** Practical or ideal solutions beam controls **X**Aggressive and conservative linac ring **Under restrictions of** accelerator physics Time, safety, budget, man-power beam delivery detector Here we think about data acquisition adaptive reliability computing

physics, chemistry, medical treatment

Reliability



Reliability Increase without much Cost

There should be "right way" We hope to have it some day, but for now we need interims

Surveillance for everything Well-arranged system does not need this, but...

Testing framework

Hardware/Middleware tests just before Beam

Software tests when installed

Redundancy

In Many Hardware/Software components

*****Of course some of them are Expensive, but...

Reliability



Surveillance for everything

We have written too many pieces of software

- which assume certain circumstances unfortunately × which will fail some day

We manage too many computers

- If only one, I'm almost sure I can make it stable
 ¤ But in reality even hostname can be mis-labeled
- We installed too many network components
 - without good network database etc
 X which sometimes has bad routing information, etc





Surveillance for everything

If certain installation of (software/hardware) was not ideal

- Find out
 - **What is the most important feature of the installation?**
 - **What is the easiest test for its healthiness?**
- Routine test is carried automatically
 - **¤ by cron or continuous scripts**
 - **¤ If an anomaly found,**
 - Alarm, e-Mail to the author, make error log
 - Restart related software, if not critical
 - Report to the human operator, if critical

Not ideal, but effective under limited human resources



Software Testing

- Moving operating environment
 - For better resource performance
 - **We tend to do it because of the pressure from budget restrictions**
 - May lead to malfunctions
 - **We knew they may happen**

Automatic software (hardware) tests preferable

- Under new environment (machine, compiler, network, etc)
 X Many kinds of important free software does them
 X I anguage evotome, Linux Test Preject
 - **X** Language systems, Linux Test Project

•We do some tests

- But sometimes not enough
- More thoroughly prepared tests needed





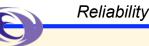


When we introduce new environment

- Unit test
 - **¤We don't do it much yet**
 - EPICS began to have it, "make runtests"
 - Collecting existent test cases
 - User can provide tests in Perl/Test framework
 - Hope to have for SAD and SADscripts
- Regression tests
 - We have something, but not thorough, not exhaustive
 - Difficult to collect cases

Stress tests

- We do it during operation (?)
- We know computers rarely fail, but network/network-devices do
 - Find solution
 - Development of surveillances
 - Installation of failure-recovery or failover procedures





Testing Framework

When we start new run

- New software/hardware
 - **We test unit by unit**
 - **But not through operational tools prepared**
- Maintenance works
 - We often forget to restore/initialize cables, switches, variables
 - **Power-stop may bring another annoyance**

We need routine procedures which include

- Hardware tests
- **Xame/ID** matching
- **¤ Database tests**
- Software component tests
- Software/Hardware simulation tests
- Before beam operation

We do it mostly by operator observations based on written procedures
 CERN did some efforts



Redundancy

Do we need redundancy? **Redundancy may be the last-resort measure** π It may cost Centralized facilities are easier to manage If I have only one server, my life is much easier But they become complicated monsters **Nobody understand everything** But especially useful for maintenance Not only for failure-recovery **Redundant systems of complicated system; (complicated)**² Anyway we may have to prepare backups Then automatic failover is just around the corner **≍And**



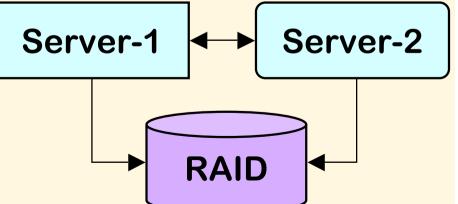
File server redundancy

RAID and Mirror-disks are used everywhere now

- **We began to use Cluster software before KEKB**
 - DECsafe, TruCluster for Unix
 - LifeKeeper, Redhat-AS, Rose-HA for Linux
 - NetApp

Reliability

It works at least for Hardware troubles; but sometimes for Software troubles Maintenance and Scheduling became easier







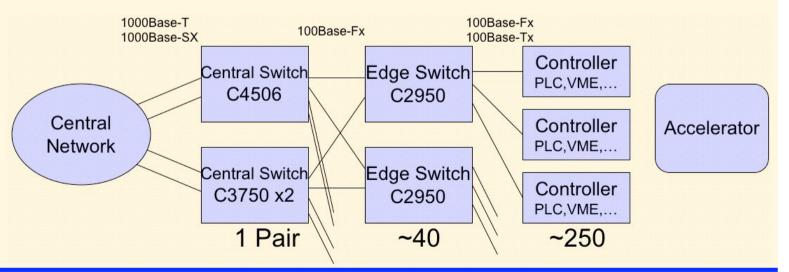
Repeater

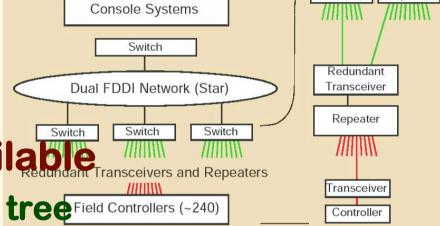
Switch

Network Redundancy

Mostly established technologies

- Wide acceptance of Ethernet and IP
- > 10 years ago
 - **¤ Redundant Transceivers**
- More recently Standards available
 - Hsrp or Vrrp and Rapid spanning tree Field Controllers (~240)









CPU built-in redundancy is already used in several vendors

Dual main memory with checksum at every-cycle

- ROM as well as flash memory
 - **¤ Bad circumstances at field forced them to implement it**

We just started to evaluate redundant CPU's

In two ways, but no man-power ...

Redundant PLC's are used at CERN

Siemens S7, slightly expensive

- Several possibilities in architecture
 - Single vs. dual backplane
 - Power-supply, CPU, Network-interface
 - **∻I/O (?)**





Redundant EPICS IOC

Redundant controllers are favorable

•as in PLCs

The project was started at DESY (M. Clausen)

- **Redundancy monitor task (RMT)**
 - Monitors healthiness of controllers
 - Manages primary redundancy resource (PRR)

Continuous control executive (CCE)

- Synchronizes internal states
- **Modifications for several others PRR's**
 - Scan tasks, Channel access server tasks, Sequencer, Drivers
 - Possibly user tasks

KEK joined in for wider applications

- Implicit Linux (OSI) port
- **Gateway applications**

***ATCA implementation underway with IPMI/OpenHPI**

≍ For ILC (?), microTCA (?)





Software redundancy

EPICS IOC redundancy is slightly complicated

- Since it has name resolution facility
- More advanced

Linac/KEK controls is simpler

- Normally we run several middle-layer control servers
 - **¤ on separate machines**
- For EPICS gateway
 - **We need redundant IOC technology**

Other existent servers

Recently more careful in redundancy

- **Like dchpd**
- **Redundancy and replications**



Summary



Phronesis

- Aristotle's view of wisdom.
- Contrary to Sophia; the ability to understand the universal truth
- Phronesis is the ability to find a way to achieve an overall goodness
- May fit Asian way of accelerator controls and operation

Summary



Accelerator controls design needs a balance between many aspects

There are many good technologies waiting to be utilized

Also more reliability features needed
 Share more experiences

Share more experiences

Phronesis

Accelerator Controls in General





